作成日 改訂日

2010年11月17日

PRODUCT SPECIFICATION

PRODUCT NAME TFT-LCD MODULE

TYPE NAME **FX-12-075**

FOXLINK	
FU GANG ELECTRONIC(KUNSHAN)CO.,LTD.	InfoVi

IVO
InfoVision Optoelectronics(Kunshan)Co.,Ltd.

WUXI SHARP ELECTRONIC COMPONENTS Co.,LTD

RECORDS OF REVISION

Model No. : FX-12-075

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1. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (<u>Thin Film Transistor</u>). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a White-LED Backlight unit. Graphics and texts can be displayed on a 800 × RGB × 600dots panel with about 1619 million colors by using LVDS (Low Voltage Differential Signaling) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and +12.0V DC supply voltage for backlight.

The TFT-LCD panel used for this module is a high-brightness and high-contrast image.

The maximum viewing angle is in the 6o'clock direction.

The 12o'clock direction is difficult to reverse the grayscale.

The LED driver circuit and the PWM circuit to drive the backlight are built into the module.

2. Mechanical specifications

Parameter	specifications	Unit
Display size	30.7 (12.1inch) Diagonal	cm
Active area	246.0 (H) × 184.5 (V)	mm
Dival farment	800 (H) × 600 (V)	n lived
Pixel format	(1pixel=R+G+B dot)	pixel
Aspect ratio	4:3	
Pixel pitch	0.3075 (H) × 0.3075 (V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions	265.0 (W) × 205.0 (H) × 9.5(D)	mm
Mass	Max. 550	g
Surface treatment	Anti-glare and hard-coating 3H	

Outline dimensions are shown in Fig.1.

3. Input Terminals

3-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V power supply)

Using connectors: FI-XPB30SRL-HF11 (Japan Aviation Electronics industry Co., Ltd.)

Corresponding connectors:

FI-X30H / FI-X30HL / FI-X30C2-NPB (Japan Aviation Electronics industry Co., Ltd.)

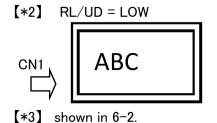
Using LVDS receiver: Building into cotroll IC(THC63LVDF84B(Thine electronics) or Compatible product)

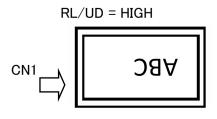
Corresponding LVDS transmitter: THC63LVDM83R(Thine electronics)

CN1

Pin	Symbol	Function	Remark
1	GND	GND	
2	SELLVDS	LVDS SET	【 *1】
3	RL/UD	Horizontal/Vertical display mode select signal	[*2]
4	GND	GND	
5	RxIN3+	LVDS receiver signal CH3 (+)	LVDS
6	RxIN3-	LVDS receiver signal CH3 (-)	LVDS
7	GND	GND	
8	CK IN+	LVDS receiver signal CK (+)	LVDS
9	CK IN-	LVDS receiver signal CK (-)	LVDS
10	GND	GND	
11	RxIN2+	LVDS receiver signal CH2 (+)	LVDS
12	RxIN2-	LVDS receiver signal CH2 (-)	LVDS
13	GND	GND	
14	RxIN1+	LVDS receiver signal CH1 (+)	LVDS
15	RxIN1-	LVDS receiver signal CH1 (-)	LVDS
16	GND	GND	
17	RxIN0+	LVDS receiver signal CH0 (+)	LVDS
18	RxIN0-	LVDS receiver signal CH0 (-)	LVDS
19	GND	GND	
20	GND	GND	
21	VCC	+3.3V Power supply	
22	VCC	+3.3V Power supply	
23	GND	GND	
24	VBR	PWM signal	[*3]
25	XSTABY	Backlight ON/OFF signal	[*3]
26	GND	GND	
27	VDD	+12V Power supply	
28	VDD	+12V Power supply	
29	GND	GND	
30	GND	GND	

[*1] SELLVDS is shown in 4-2.



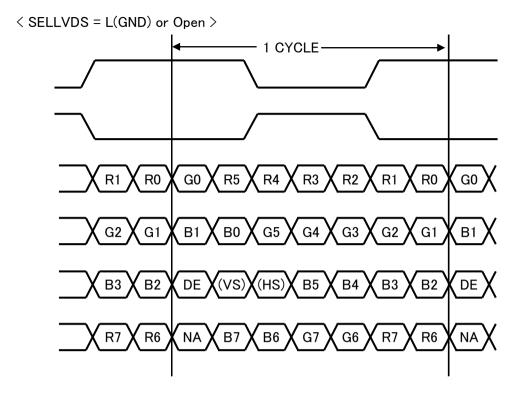


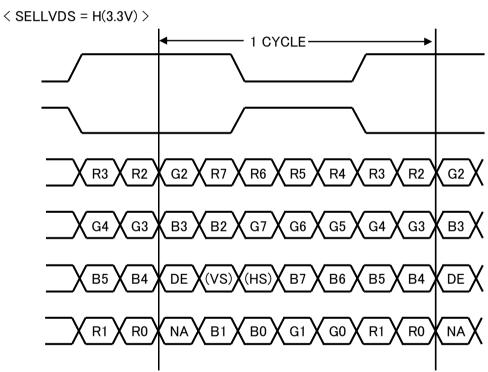
3-2. Data Mapping

1) 8 bit input

[*1] pin assignment with SELLVDS pin (THC63LVDM83R(Thine electronics))

Transmitter		2Pin SI	ELLVDS		
Pin No	Data	= L(GND) or Open	= H(3.3V)		
51	TA0	R0 (LSB)	R2		
52	TA1	R1	R3		
54	TA2	R2	R4		
55	TA3	R3	R5		
56	TA4	R4	R6		
3	TA5	R5	R7 (MSB)		
4	TA6	G0 (LSB)	G2		
6	TB0	G1	G3		
7	TB1	G2	G4		
11	TB2	G3	G5		
12	TB3	G4	G6		
14	TB4	G5	G7 (MSB)		
15	TB5	B0 (LSB)	B2		
19	TB6	B1	B3		
20	TC0	B2	B4		
22	TC1	В3	B5		
23	TC2	B4	В6		
24	TC3	B5	B7 (MSB)		
27	TC4	(HS)	(HS)		
28	TC5	(VS)	(VS)		
30	TC6	DE	DE		
50	TD0	R6	R0 (LSB)		
2	TD1	R7 (MSB)	R1		
8	TD2	G6	G0 (LSB)		
10	TD3	G7 (MSB)	G1		
16	TD4	В6	B0 (LSB)		
18	TD5	B7 (MSB)	B1		
25	TD6	(NA)	(NA)		





DE: DATA ENABLE

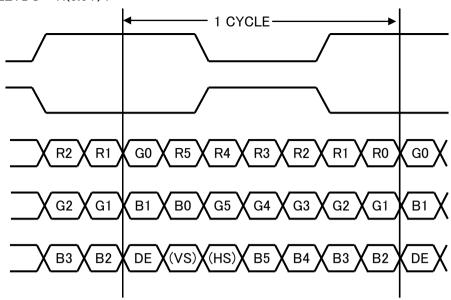
HS:Hsync VS:Vsync

2) 6bit input

[*1] pin assignment with SELLVDS (THC63LVDM83R(Thine electronics))

Transmitter		2Pin SELLVDS		
Pin No	Data	= L(GND) or Open	= H(3.3V)	
51	TA0	-	R0 (LSB)	
52	TA1	_	R1	
54	TA2	-	R2	
55	TA3	-	R3	
56	TA4	-	R4	
3	TA5	-	R5 (MSB)	
4	TA6	-	G0 (LSB)	
6	TB0	-	G1	
7	TB1	-	G2	
11	TB2	-	G3	
12	TB3	-	G4	
14	TB4	-	G5 (MSB)	
15	TB5	-	B0 (LSB)	
19	TB6	-	B1	
20	TC0	-	B2	
22	TC1	-	В3	
23	TC2	-	B4	
24	TC3	-	B5 (MSB)	
27	TC4	-	(HS)	
28	TC5	-	(VS)	
30	TC6	-	DE	
50	TD0	-	GND	
2	TD1	-	GND	
8	TD2	-	GND	
10	TD3	-	GND	
16	TD4	-	GND	
18	TD5	-	GND	
25	TD6	-	(NA)	

< SELLVDS = H(3.3V) >

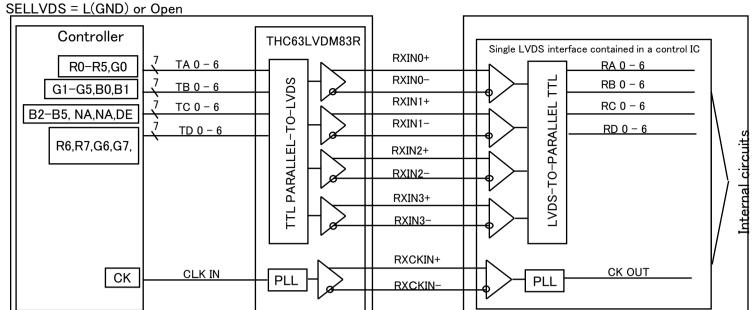


DE:DATA ENABLE

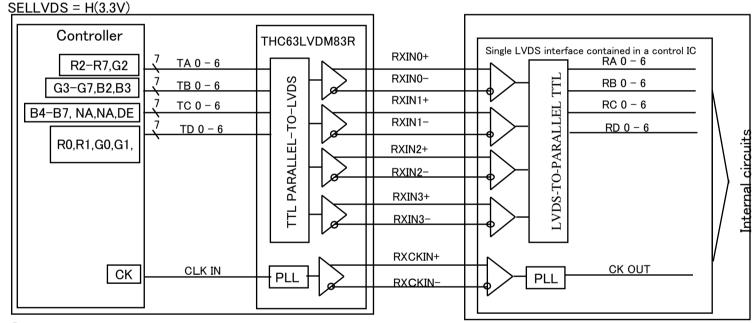
HS:Hsync VS:Vsync

3-3. Interface block diagram

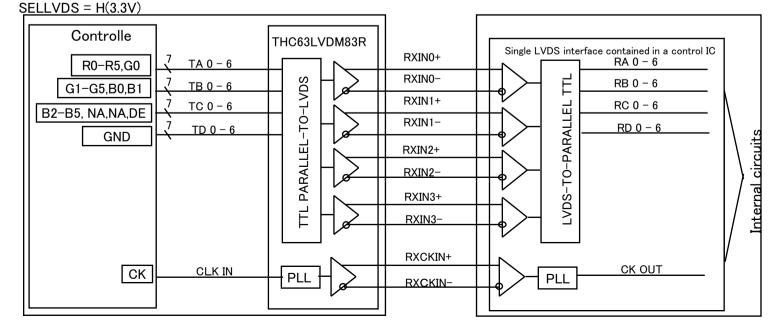
①8 bit input (Computer Side) (TFT-LCD side)



28 bit input



36 bit input



4. Absolute Maximum Ratings

Parameter	Symbol	Condition	Pin	Ratings	Unit	Remark
Supply voltage	Vcc	Ta=25°C	VCC	-0.3 ~ +4.0	V	[*1,2]
	V _{DD}	Ta=25°C	VDD	-0.3 ~ +15.0	V	【*1,2】
	V	Ta=25°C	RxINi-/+	-0.3∼Vcc+0.3	٧	:-0.1.0.0
	V _{I 1}	1a-25 C	CK IN-/+	-0.3∼ vcc+0.3	V	i=0,1,2,3
Input voltage	V _{I 2}	Ta=25°C	RL/UD,SELLVDS	-0.3∼Vcc+0.3	V	
	V _{I 4}	Ta=25°C	XSTABY, VBR	-0.3 ~ +VDD	V	
Storage temperature	T _{STG}	_	− − -30 ~ +75		°C	[*1]
Operating temperature	T _{OPA}	_	_	-10 ~ +75	°C	[*1,3,4]

[*1] Humidity:95%RH Max.(Ta≤40°C) Note static electricity.

Maximum wet-bulb temperature at 39°C or less. (Ta>40°C) No condensation.

- [*2] The Vcc power supply capacity must use the one of 2A or more.
 - The Vcc power supply capacity must use the one of 3A or more.
- [*3] There is a possibility of causing deterioration in the irregularity and others of the screen and the display fineness though the liquid crystal module doesn't arrive at destruction when using it at $65 \sim 75$ °C.
- [*4] In the operating temperature item, the low temperature side is the ambient temperature regulations.

 The high temperature side is the panel surface temperature regulations.

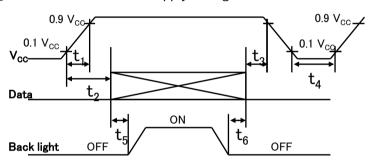
5. Electrical Characteristics

5-1. TFT-LCD panel driving

T _a =+25°	С
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Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Supply voltag	е	V _{cc}		3.0	3.3	3.6	V	【*1】
Current dissipat	tion	I_{CC}	Vcc=3.3V	-	260	390	mA	【*2】
Input voltage width for LVDS	S receiver	V_L		0	_	2.4	٧	
Permissive input ripple	voltage	V_{RP}		_	_	200	mV_{P-P}	Vcc=3.3V
Differential input	High	V_{TH}		_	_	V _{CM} +100	mV	V _{CM} =+1.2V
Threshold voltage	Low	V_{TL}		V _{CM} -100	_	_	mV	【*3】
In much wealth a me		V_{IH}		2.1	_	_	V	[*4]
Input voltage	;	V_{IL}			_	0.8	V	
Input reak current		I _{OH}			_	400	μΑ	V ₁₂ =+3.3V [*4]
		I _{OL}		-10	_	+10	μΑ	V ₁₂ =0V [*4]
Terminal resist	or	R _T		_	100	_	Ω	Differential input

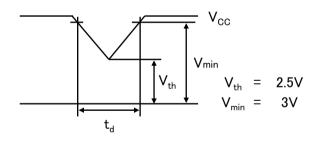
[*1] On-off conditions for supply voltage



 $\begin{array}{ccccc} 0 & < & t_1 & \leqq & 10ms \\ 0 & < & t_2 & \leqq & 20ms \\ 0 & < & t_3 & \leqq & 1s \\ 1s & \leqq & t_4 & & & \\ 500ms & \leqq & t_5 & & & & \end{array}$

 $200 \text{ms} \leq t_6$

Vcc-dip conditions



- . Vth < V $_{CC} \leqq$ Vmin $t_d \leqq 10 ms$
- \cdot V_{CC} < V_{th}

 $\label{lem:conditions} \mbox{Vcc-dip conditions should also follow the On-off conditions for supply voltage}$

- Hsync/Vsync need not be input so that this model may drive only by the ENAB signal.
 Even if Hsync/Vsync is input, it doesn't become a malfunction.
- The relation between the data input and the backlight lighting will recommend the above-mentioned input sequence. When the backlight is turned on before the panel operates, there is a possibility of abnormally displaying.

 The liquid crystal module is not damaged.

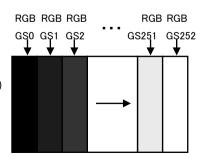
[*2] Current dissipation

Typical current situation : 253-gray-bar pattern

 $(Measurement\ condition: Vcc=+3.3V, fck=40MHz, Ta=25^{\circ}C)$

Regarding RGB gradation, refer to Chapter 8

- 【注3】 V_{CM}: LVDS common mode voltage
- 【注4】RL/UD, SELLVDS

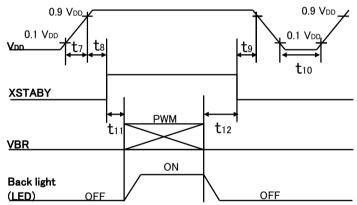


5-2. LED backlight

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Supply voltage	V _{DD}	10.2	12.0	13.8	٧	[*1]
Current dissination	I DD1	-	350	500	mA	【*2】
Current dissipation	I DD2	-	-	10	μΑ	[*3,4]
Permissive input ripple voltage	VRP_BL	-	-	200	mV _{P-P}	VDD=+12.0V
BL1 input high voltage	VIH_BL1	2.4	-	VDD	V	【*3】
BL1input Low voltage	VIL_BL1	-	-	0.2	V	【*3】
BL2 input high voltage	VIH_BL2	2.1	-	VDD	V	【*4】
BL2 input low voltage	VIL_BL2	ı	ı	0.8	V	【*4】
PWM frequency	fрwм	200	-	1K	Hz	【*4,5】
PWM duty	Dрwм	50	_	100	%	【*4,5】
Life time	L	_	(50,000) (Module)	_	h	【Reference】 【*6】

[*1] On-off conditions for supply voltage



 $0ms \le t8$

0ms ≤ t9

200ms ≦ t10

10ms ≦ t11

0ms ≦ t₁₂

[*2] Current dissipation

Typ. value: VDD=+12.0V, Duty=100% Max. value: VDD=+10.2V, Duty=100%

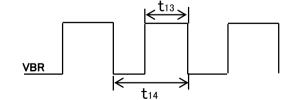
[*3] XSTABY

[*4] VBR

[*5] PWM

 $f_{PWM} = 1/t_{14}$

Duty 0%: Min. Luminance
Duty 100%: Max. Luminance



Luminance changes in proportion to the duty ratio. (t₁₃≧10µ s)

When the frequency slows, the display fineness might decrease.

PWM frequency may interfere with drive frequency and generate wavy noise on the display.

To avoid this symptom, please keep PWM frequency as different frequency from drive frequency and its harmonic frequency as possible while using.

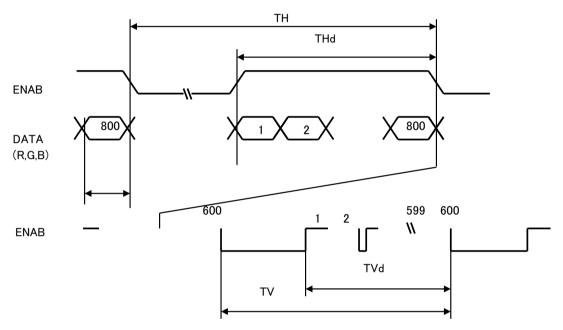
[*6] Luminance becomes 50% of an initial value. (Ta=25°C, PWM=100%)

6. Timing characteristics of input signals

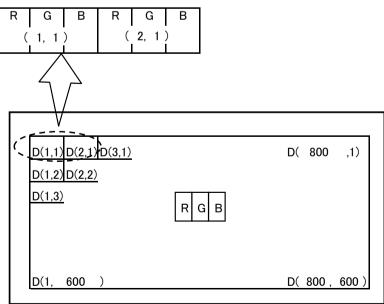
6-1. Timing characteristics

Р	arameter	Symbol	Min.	Тур.	Max.	Unit	Remark	
Clock	Frequency	1/Tc	35	40	42	MHz		
	Havinantal maviad	TH	940	1056	1395	clock		
	Horizontal period	IП	23.5	26.4	39.9	μs		
ENAB	Horizontal period (High)	THd	800	800	800	clock		
ENAD	Martia al Fua sucasa.	TV	628	666	798	line	[*1]	
	Vertical Frequency	IV	_	16.7	-	ms	7 → 1 1	
	Vertical period (High)	TVd	600	600	600	line		

[*1] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



6-2. Input Data Signals and Display Position on the screen



7. Input Signals, Basic Display Colors and Gray Scale of Each Color

7-1. 8 bit input

		Data signal																								
	Colors & Gray scale	Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	В1	B2	В3	В4	В5	В6	В7
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
٦	Green	_	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Color	Cyan	_	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
Basic	Red	_	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ä	Magenta		Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
	Yellow	_	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	_	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ъ	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
f Red	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le of	1	1				1	1				1 1															
Scale	\downarrow	1	↓								1								\downarrow							
Gray	Brighter	GS250	1	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
В	1	GS251	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
eu	1	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
of	1	1				1	1								1								1			
cale	\downarrow	\downarrow				1	ļ							,	l								ļ			
Gray S	Brighter	GS250	0	0	0	0	0	0	0	0	1	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0
ਠੁ	1	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
υ	1	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Blue	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Scale of	1	1				1	<u> </u>							,	1								1			
Scal	\downarrow	\downarrow				1	l								l_								Į_			
Gray (Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1	1	1
9	Ţ	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1

0:Low level voltage 1:High level voltage X:Don't care

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16-million-color display can be achieved on the screen.

7-2. 6 bit input

	Colors &		Data signal																		
	Gray scale	GrayScale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	В1	B2	ВЗ	В4	B5	
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	
,	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
Basic Color	Cyan	1	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
asic	Red	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
B	Magenta	ı	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	
	White	ı	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
70	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
f Re	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ile o	1	\downarrow			,	l					ļ	ļ					,	l			
Gray Scale of Red	\downarrow	\downarrow	↓							↓						1					
àray	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	\downarrow	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
en	1	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	
Gre	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	
e of	1	\downarrow			,	l			1								,	l			
Scale of Green	\downarrow	\downarrow			,	l			\downarrow								,	l			
Gray (Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	
g	\downarrow	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
<u>o</u>	1	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	
f Blu	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
Gray Scale of Blue	1	\downarrow			,	l					1	ļ					`	l			
Sca	\downarrow	\downarrow			,	l						,			↓						
згау	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	
	\downarrow	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

8. Optical Characteristics

Ta=+25°C, Vcc=+3.3V

Parai	Parameter		Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Horizontal	θ 21,θ 22		60	80	-	Deg.	
angle	Vertical	θ 11	CR>10	35	60	-	Deg.	【*1,2,4】
range	vertical	θ 12		60	80	-	Deg.	
Contra	st ratio	CR	optimized angle	500	800	-		【*2,4】
Response Time	White Black	тr+т d		-	30	-	ms	【*3,4】
Chroma	ticity of	Wx		0.240	0.290	0.340		
Wh	White			0.255	0.305	0.355		
Chroma	ticity of	Rx			0.650			
R	ed	Ry			0.330			[*4]
Chroma	ticity of	Gx	θ =0°		0.310			[*4]
Gre	een	Gy	1 0 -0		0.640			
Chroma	ticity of	Bx			0.150			
ВІ	ue	Ву			0.060			
Luminanc	Luminance of white			260	330	-	cd/m²	[*4]
White U	niformity			-	-	1.33		【*5】

XThe measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown \square in Fig.2 below.

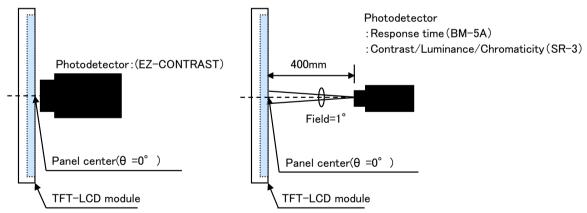
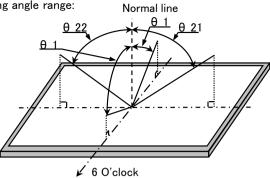


fig.2-1 Measuring method of Viewing angle range.

fig.2-2 Measuring method of contrast, luminance, response time, and Chromaticity.

Fig.2 Optical characteristics measurement method

[*1] Definitions of viewing angle range:

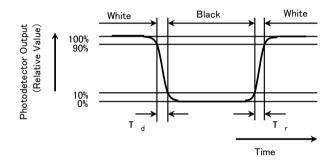


[*2] Definition of contrast ratio:

The contrast ratio is defined as the following. Contrast (CR) = Luminance with all pixels white Luminance with all pixels black

[*3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

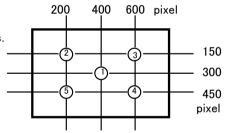


[*4] This shall be measured at center of the screen.

[*5] Definition of white uniformity:

White uniformity is defined as the following with five measurements. $(\widehat{\ })\sim\widehat{\ }$

 $\delta_{w} = \frac{\text{Maximum luminance of 5 points}(1) \sim (5)}{\text{Maximum luminance of 5 points}(1) \sim (5)}.$



9. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Since the front polarizer is easily damaged, pay attention not to scratch it.
- c) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- d) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- e) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- f) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling. Observe all other precautionary requirements in handling components.
- g) Since there is a circuit board in the module back, stress is not added at the time of a design assembly. Please make it like. If stress is added, there is a possibility that circuit parts may be damaged.
- h) It causes an irregular display and the defective indication, etc., when always put constant pressure on the back of the module.
 - Please do not make the structure to press the back of the module.
- i) Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
- j) Connect GND to stabilize against EMI and external noise.
- k) When handling LCD modules and assembling them into cabinets, please avoid that long-terms storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the modules. Do not use the LCD module under such environment.
- I) Please do not pull, and do not hang LED_FPC at the installation of the module.
- m) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- n) Be careful when using it for long time with fixed pattern display as it may cause accidential image.
- o) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- p) If a minute particle enters in the module and adheres to an optical material, it may cause display non-uniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.
- q) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- Notice: Never take to pieces the module, because it will cause failure.
 Please do not peel off the Black tape pasted to the product.
- s) An abnormal display by changing in quality of the polarizing plate might occur regardless of contact or no contact to the polarizing plate, because of epoxy resin (amine system curing agent) that comes out from the material and the packaging material used for the set side, the silicon adhesive (dealcoholization system and oxime system), and the tray blowing agents (azo-compound), etc. Please confirm adaptability with your employed material.

10. Packing form

a) Piling number of cartons : MAX. 5

b) Package quantity in one carton: 20pcs

c) Carton size(TYP): $504mm(W) \times 426mm(D) \times 326mm(H)$

d) Total mass of one carton filled with full modules(20pcs): 14kg

11. Reliability test items

No.	Test item	Conditions	Remark
1	High temperature storage test	Ambient temperature 75°C 240H	[Note1]
2	Low temperature strage test	Ambient temperature −30°C 240H	【Note1】
3	High temperature & high humidity operation test	Ambient temperature 40°C, Humidity 95% RH 240H (No condensation.)	【Note1】
4	High temperature operation test	Panel surface 75°C 240H	【Note1】
5	Low temperature operation test	Ambient temperature −10°C 240H	[Note1]
6	Vibration test	<pre> <sin wave=""> Frequency :10~57Hz / Vibration width (one side) :0.076mm</sin></pre>	【Note1】
7	Shock test	Max. gravity:490m/s2 Pulse width:11ms Direction: ±X,±Y,±Z Test period:1time ✓1direction	[Note1]
8	Thermal shock test	−30°C[0.5h] ~ 75°C[0.5h]∕50cycles	【Note1】

[Note1] Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function. (normal operation state: Temperature:15~35°C, Humidity:45~75%, Atmospheric pressure:86~106kpa)

12. Others

12-1. Lot No Label

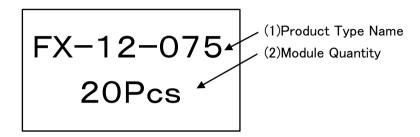
A) Module Serial Label

Label of product type name is pasted on the backside of the module as shown below.

FX-12-075

12-2. Packing Box Label

Label is pasted on packing box and it shows (1)Product Type Name (2)Module Quantity



13. Storage conditions

Environmental condition range of storage temperature and humidity

Temperature 0 to 40 degrees Celsius

Relative humidity 95% and below

[Note] Please refer below as a mean value of the environmental conditions.

Summer time temperature 20 to 35 degrees Celsius humidity , 85% and below

Winter time temperature 5 to 15 degrees Celsius humidity, 85% and below

Please maintain within 240 hours of accumulated length of storage time, with conditions of 40 degrees Celsius and room humidity of 95%.

Direct sun light

Please keep the product in a dark room or cover the product to protect from direct sun light.

Atmospheric condition

Please refrain from keeping the product with possible corrosive gas or volatile flux.

Prevention of dew

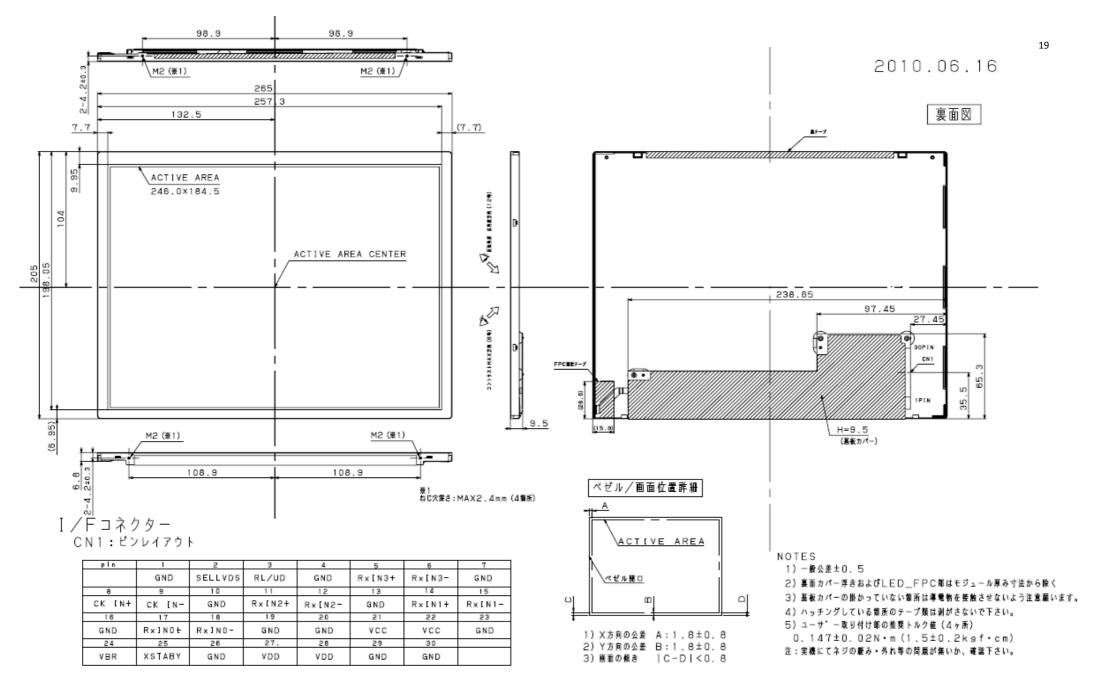
Please store the product carton either on a wooden pallet or a stand / rack to prevent dew.

Do not place directly on the floor. In addition, to obtain moderate ventilation in between the pallet's top and bottom surfaces, pile the cartons up in a single direction and in order.

Please place the product cartons away from the storage wall.

Storage period

Within above mentioned conditions, maximum storage period should be one year.



適合コネクタ:F[-X30H/F[-X30HL/F[-X30C2-NPB(航空電子工業報)又は同等性能品

図1:モジュール外形図

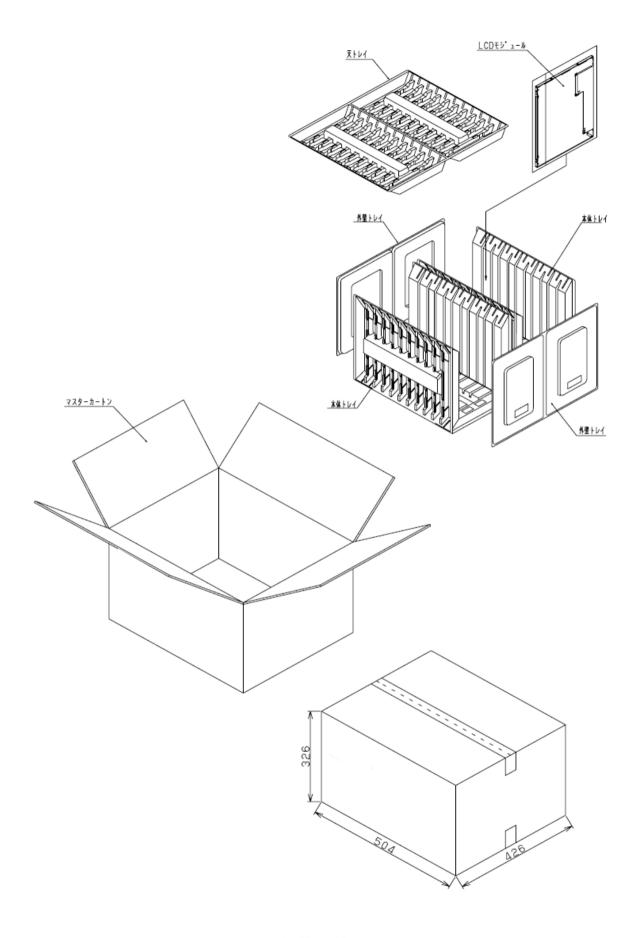


図3 包装形態図